

**Subject:** Analog opening at Finisar

**From:** Jonathan Ashbrook <jonathan.ashbrook@finisar.com>

**Date:** 2/25/2016 8:34 PM

**To:** "Schutt-Aine, Jose E" <jesa@illinois.edu>

Jose,

I hope the spring semester is going well for you. It was probably a harsh reality to come back to Illinois after spending time in Vietnam!

We have recently had an analog/RF design req open up for our office, and I think we should be getting another one soon. As I mentioned in January, our office has been growing quickly. We are up to 8 engineers, and are wrapping up the interview process for a 9<sup>th</sup>.

Demand for our ICs within Finisar is booming and we are working on a number of exciting projects. All of our projects are for 28Gb/s or 56Gb/s links. Some of these projects involve advanced modulation or advanced channel equalization.

We now have four senior analog designers in Champaign, so we have an excellent mentoring environment for a young engineer to take advantage of.

If any of your students (MS or PhD) are graduating and might be interested in this position, please have him or her contact me directly.

Thanks,  
Jonathan

---

Jonathan Ashbrook  
Engineering Manager  
IC Engineering  
Finisar Corporation  
[jonathan.ashbrook@finisar.com](mailto:jonathan.ashbrook@finisar.com)  
(w) +1 217-974-7010  
(f) +1 217-351-1017